INTERNAL ASSESMENT

Evaluation - Distribution and Weightage of marks

1. The performance of a student in every subject/course (including practicals and Project Stage – I & II) will be evaluated for 100 marks each, with 25 marks allotted for CIE (Continuous Internal Evaluation) and 75 marks for SEE (Semester End-Examination). For theory subjects, during a semester, there shall be two mid-term examinations. Each mid-term examination consists of one objective paper, one descriptive paper and one assignment. The objective paper and the descriptive paper shall be for 10 marks each with a total duration of 1 hour 20 minutes (20 minutes for objective and 60 minutes for descriptive paper).

2. The objective paper is set with 20 multiple choice, fill in the blanks and matching type of questions for a total of 10 marks. The descriptive paper shall contain 4 full questions out of which, the student has to answer 2 questions, each carrying 5 marks. While the first mid-term examination shall be conducted on 50% of the syllabus, the second mid-term examination shall be conducted on the remaining 50% of the syllabus. Five marks are allocated forassignments (as specified by the subject teacher concerned). The first assignment should be submitted before the conduct of the first mid-term examination, and the second assignment should be submitted before the conduct of the second mid-term examination. The total marks secured by the student in each mid-term examination are evaluated for 25 marks, and the average of the two mid-term examinations shall be taken as the final marks secured by each student in Continuous Internal Evaluation.

3. If any student is absent from any subject of a mid-term examination, an on-line test will be conducted for him by the University.

PRINCETON INSTITUTE OF ENGINEERING & TECHNOLOGY FOR WOMEN

ChowdaryGuda-[V], Narapally, Ghatkesar .Mandal, R.R. District -501 301
(Approved by AICTE, Affiliated to JNTUH, Hyderabad)

III B.Tech I Semester (R18– Regulation) (2022 – 2023)
II– Mid Term Examination, Nov– 2023

Branch : ECE   Date of Exam:21ST Jan 2023

Subject: CO & OS   Session & Timings: FN 9.40 AM to 11.00 AM.

Answer any two of the following Questions. Each Question carry 5 marks, Maximum Marks: 10 M

Question No. & Sub Question No.		Questions	Question Wise Marks
1.	a)	Explain the OS structure and its components?	2.5
	b)	Explain about the distributed system in data?	2.5
2.	a)	Explain about computer environment?	2.5
	b)	Expain about parallel system inbriefy?	2.5
3.	a)	Describe various files allocation method briefly	2.5
	b)	Explain the concept of file system?	2.5
4.	a)	Explain briefy about file sharing and file system mounting?	2.5
	b)	Explain about swapping in memory management?	2.5

PRINCETON INSTITUTE OF ENGINEERING AND TECHNOLOGY FOR WOMEN

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(Approved by AICTE & Affiliated to JNTUH)
Chowdharyguda, Korremula(V),Ghatkesar (M),Medchal(D),TS-500088
III B.Tech I Semester (R18 – Regulation) (2022 – 2023)
II – Mid Term Examination, JAN – 2023

Branch : ECE   **Date of Exam: 21-01-2023**

Subject:– CO & OS   **Session & Timings: FN 9.40 AM to 11.00 AM**

OBJECTIVE EXAM

Name: Hall-Ticket No.:

Answer All Questions. All Questions Carry Equal Marks. Time: 20 Min. Marks: 10.

SET-I

1	What is an operating system? a) interface between the hardware and application programs b) collection of programs that manages hardware resources c) system service provider to the application programs d) all of the mentioned	( )
2	What is the main function of the command interpreter? a) to provide the interface between the API and application program b) to handle the files in the operating system c) to get and execute the next user-specified command d) none of the mentioned	( )
3	In Operating Systems, which of the following is/are CPU scheduling algorithms? a) Priority b) Round Robin c) Shortest Job First d) All of the mentioned	( )
4	To access the services of the operating system, the interface is provided by the _ a) Library b) System calls c) Assembly instructions d) API	( )
5	. CPU scheduling is the basis of _____ a) multiprogramming operating systems b) larger memory sized systems c) multiprocessor systems d) none of the mentioned	( )
6	Which one of the following is not true? a) kernel remains in the memory during the entire computer session b) kernel is made of various modules which can not be loaded in running operating system c) kernel is the first part of the operating system to load into memory during booting d) kernel is the program that constitutes the central core of the operating system	( )

7	Which one of the following errors will be handle by the operating system? a) lack of paper in printer b) connection failure in the network c) power failure d) all of the mentioned	
8	Where is the operating system placed in the memory? a) either low or high memory (depending on the location of interrupt vector) b) in the low memory c) in the high memory d) none of the mentioned	( )
9	If a process fails, most operating system write the error information to a _____ a) new file b) another running process c) log file d) none of the mentioned	( )
10	Which one of the following is not a real time operating system? a) RTLinux b) Palm OS c) QNX d) VxWorks	( )

Fill in the blanks   10 x ½=5

1._____ controls the way in which the computer system functions and provides a means by which users can interact with the computer.

2. _____ may be included in other, folder while making hierarchical structure folder.

3. Linux is a _____ operating system.

4. _____ are lists of commands that appear on the screen.

5. The _____ tells the computer how to use its components.

6. MS-DOS is a _____operating system.

7. To start an I/O operation the device driver loads the appropriate register into is said to be _____.

8. The maximum length of the filename in DOS is _____.

9. A command interpreter called _____.

10. The full name of FAT _____.

PRINCETON INSTITUTE OF ENGINEERING AND TECHNOLOGY FOR WOMEN

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Chowdharyguda, Korremula(V),Ghatkesar (M),Medchal(D),TS-500088
III B.Tech I Semester (R18– Regulation) (2022 – 2023)
<u>I – Mid Term Examination, NOV– 2022</u>

Branch :ECE   Date of Exam: 12 NOV **-2022**

Subject: Control System

Session & Timings: FN 9.40 AM to 10.40 AM

Answer any TWO of the following Questions. Each Question carry 5 marks, Maximum Marks: 10 M

Question No. & Sub Question No.		Questions	Course Outcome Level	Blooms Tx Level	Question Wise Marks
1.	a)	Explain open loop system with example	CO1	BTL4	2.5
	b)	Compare Open loop and Close Loop System	CO2	BTL4	2.5
2.	a)	**What is Time Response explain in details**	CO4	BTL2	2.5
	b)	Explain first order system with unit step I/P.	CO2	BTL2	2.5
3.	a)	Find y Y(s)/R(s)	CO2	BTL4	5
4.	a)	**Find y6/y1**	C02	BTL4	5

PRINCETON INSTITUTE OF ENGINEERING AND TECHNOLOGY FOR WOMEN

(Approved by AICTE & Affiliated to JNTUH)
Chowdharyguda, Korremula(V),Ghatkesar (M),Medchal(D),TS-500088
III B.Tech I Semester (R18– Regulation) (2022 – 2023)

<u>I – Mid Term Examination, NOV– 2022</u>

Branch :ECE   Date of Exam: 12 NOV **-2022**

Subject: Control System

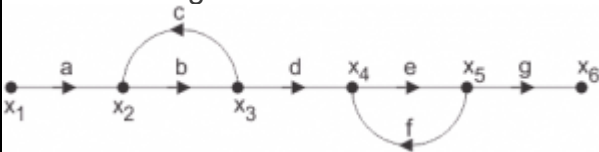Session & Timings: FN 10.40 AM to 11.0 AM

OBJECTIVE EXAM

Name:

Hall-Ticket No.:										

Answer All Questions. All Questions Carry Equal Marks.   Time: 20 Min. Marks: 10.

SET- I

1	Which of the following is not the feature of modern control system? a) Quick response b) Accuracy c) Correct power level d) No oscillation	( )
2	A linear system at rest is subject to an input signal $r(t)=1-e^{-t}$. The response of the system for t>0 is given by $c(t)=1-e^{-2t}$. The transfer function of the system is: a) (s+2)/(s+1) b) (s+1)/(s+2) c) 2(s+1)/(s+2) d) (s+1)/2(s+2	( )
3	In regenerating the feedback, the transfer function is given by a) C(s)/R(s)=G(s)/1+G(s)H(s) b) C(s)/R(s)=G(s)H(s)/1-G(s)H(s) c) C(s)/R(s)=G(s)/1+G(s)H(s) d) C(s)/R(s)=G(s)/1-G(s)H(s)	( )
4	When deriving the transfer function of a linear element a) Both initial conditions and loading are taken into account b) Initial conditions are taken into account but the element is assumed to be not loaded c) Initial conditions are assumed to be zero but loading is taken into account d) Initial conditions are assumed to be zero and the element is assumed to be not loaded	( )
5	. If the initial conditions for a system are inherently zero, what does it physically mean? a) The system is at rest but stores energy b) The system is working but does not store energy c) The system is at rest or no energy is stored in any of its part d) The system is working with zero reference input	( )
6	. When deriving the transfer function of a linear element a) Both initial conditions and loading are taken into account b) Initial conditions are taken into account but the element is assumed to be not loaded c) Initial conditions are assumed to be zero but loading is taken into account d) Initial conditions are assumed to be zero and the element is assumed to be not loaded	( )

7	. If the initial conditions for a system are inherently zero, what does it physically mean? a) The system is at rest but stores energy b) The system is working but does not store energy c) The system is at rest or no energy is stored in any of its part d) The system is working with zero reference input	( )
8	Traffic light system is the example of: a. Open-loop system b. Closed-loop system c. Both (a) and (b) d. None of these	( )
9	Use mason's gain formula to find the transfer function of the given signal flow graph: a) abd/1-(ac) b) abdeg/1-(bc+ef)+bcef c) abd/1-(bc+ef)+bcef d) adcdef/1-(bc+ef)+bcef	( )
10	First order system is defined as : a) Number of poles at origin b) Order of the differential equation c) Total number of poles of equation d) Total number of poles and order of equation	( )

Fill in the blanks   **10 x ½=5**

11. In a control system the output of the controller is given to_____.
12. A unit step is applied at t=0 to a first order system without time delay. The response has the value of 1.264 units at t=10 mins, and 2 units at steady state. The transfer function of the system is_____.
13. A node having only outgoing branches………………………………….
15. Traffic light system is the example of ………………………………………………

16. The principle of homogeneity and superposition are applied to:………………………………..

17 The output of the feedback control system must be a function of _____

18. The output of the feedback control system must be a function of………………………

19. A control system working under unknown random actions is called _____

20. The output of the feedback control system must be a function of_____

III B.Tech I Semester (R18– Regulation) (2022 – 2023)
I – Mid Term Examination, Nov– 2022

Branch & Section: III ECE   Date of Exam: 12-Nov-2022

Subject: DCN   Session & Timings: FN-11:40 PM to 12:40 PM

Answer any TWO of the following Questions. Each Question carry 5 marks, Maximum Marks: 10 M

Question No.& Sub Question No.	Questions	Question Wise Marks	Course Outcome Level	Blooms Tx Level
1.	Define OSI Reference Model and explain the OSI layers?	5	CO4	BTL 4
2.	What is Data Link Layer and explain about Data Link Layer Flow controls	5	CO1	BTL 3
3.	Explain the characteristics of guided and unguided transmission media.	5	CO2	BTL 4
4.	What is addressing and explain types of addressings..	5	CO4	BTL 3

IV B.Tech I Semester (R18– Regulation) (2021-22)
I – Mid Term Examination, November – 2022

Branch & Section: IV ECE (A)　　　　　　Date of Exam: 02ⁿᵈ **NOV-2022**

Subject: **Digital Image Processing**

　　　　　　　　　　　　　　　　　　　Session & Timings: FN & 11.40 AM to 12.40 PM

Answer any TWO of the following Questions. Each Question carry 5 marks, Maximum Marks: 10 M

Question No. & Sub Question No.		Questions	Question Wise Marks	Course Outcome Level	BloomsTx Level
1.	a)	What are the fundamental steps in digital image processing?	3	CO1	BTL2
	b)	Define Sampling and quantization?	2	CO1	BTL1
2.	a)	Write about Walsh transform and Haar wavelet transform?	3	CO1	BTL2
	b)	What is an image sensing and acquisition	2	CO1	BTL2
3.	a)	Explain Ideal low pass filter using image enhancement in frequency domain?	5	CO2	BTL2
4.	a)	Explain Butter-Worth high pass filter using image sharpening in frequency domain?	3	CO2	BTL2
	b)	Define bit-plane splicing?	2	CO2	BTL1

IV B.Tech I Semester (R18– Regulation) (2021-22)
II – Mid Term Examination, FEB-2022

Branch & Section: IV ECE (A)   Date of Exam:

Subject: **Digital Image Processing**   Session & Timings:

Answer any TWO of the following Questions. Each Question carry 5 marks, Maximum Marks: 10 M

Question No. & Sub Question No.		Questions	Question Wise Marks	Course Outcome Level	BloomsTx Level
1.	a)	Explain details about noise models?	5	CO2	BTL2
2.	a)	Explain about fundamentals operations in morphological image processing?	3	CO3	BTL2
	b)	Define point detection.	2	CO3	BTL1
3.	a)	Explain threshold based segmentation?	5	CO2	BTL2
4.	a)	Write about an overview of image compression standards?	4	CO4	BTL2
	b)	Define quantization.	1	CO4	BTL1

Question Paper : SET1	Name:-	Register Number :

PRINCETON INSTITUTE OF ENGINEERING & TECHNOLOGY FOR WOMEN

ChowdaryGuda-[V], Narapally, Ghatkesar .Mandal, Medchal. District -500088

(Affiliated to JNTUH, Hyderabad)

Mid - I Internal Assessment test, November-2022

Degree: B-Tech.	**Department of Electronics and Communication Engineering**	**Year/Semester** : III / I
Time: 1:00 Hours	**EC501PC - Microprocessors & Microcontrollers**	**Maximum Marks:** 10

Course Outcomes (COs) for Assessment in this test

CO 1	Understands the internal architecture and organization of 8086, 8051 and ARM processors/controllers
CO 2	Understands the interfacing techniques to 8086 and 8051 and can develop assembly language programming to design microprocessor/ micro controller based systems.

CL-Cognitive Level; **Kn**-Knowledge; **Un**-Understand; **Ap**-Apply; **An**-Analyze; **Ev**-Evaluate; **Cr**-Create

Answer Any One Question from Each Part

Q.No.	PART – A	CL	CO Mapping	Mark
1.a)	What is 8086 microprocessor?	Kn	CO1	1
b)	Define Opcode and Operand?	Un	CO2	1
c)	List the types of flags?	Ap	CO2	1
d)	List various registers of the bus interface unit of 8086?	An	CO1	1
e)	What are memory addresses?	Un	CO1	1
2.a)	What is microcontroller?	Kn	CO1	1
b)	Write about PSW used in 8051 microcontroller?	An	CO2	1
c)	Write any two features of 8051?	Un	CO1	1
d)	What is the importance of Jump instruction in ALP for 8051?	Un	CO1	1
e)	Draw the format of PCON registers?	Un	CO2	1
	PART –B			
3.a)	Draw the pin diagram of 8086 Microprocessor and explain the function of each pin in detail?	An	Co2	2.5
b)	Explain about the instruction set of 8086 with examples?	An	Co2	2.5
4.a)	Explain the addressing modes used in 8086 with examples?	An	Co2	2.5
b)	Draw and explain about the 8086 architecure?	An	Co2	2.5